



Europäisches Patentamt
European Patent Office
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(11) **EP 1 202 564 A2**

(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:
02.05.2002 Bulletin 2002/18

(51) Int Cl.⁷: **H04N 5/44**

(21) Application number: **01308727.5**

(22) Date of filing: **12.10.2001**

(84) Designated Contracting States:
**AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR**
Designated Extension States:
AL LT LV MK RO SI

(72) Inventor: **Kawai, Satoshi,**
c/o Alps Electrical Co. Ltd.
Tokyo (JP)

(30) Priority: **26.10.2000 JP 2000332143**

(74) Representative: **Kensett, John Hinton**
Saunders & Dolleymore,
9 Rickmansworth Road
Watford, Hertfordshire WD18 0JU (GB)

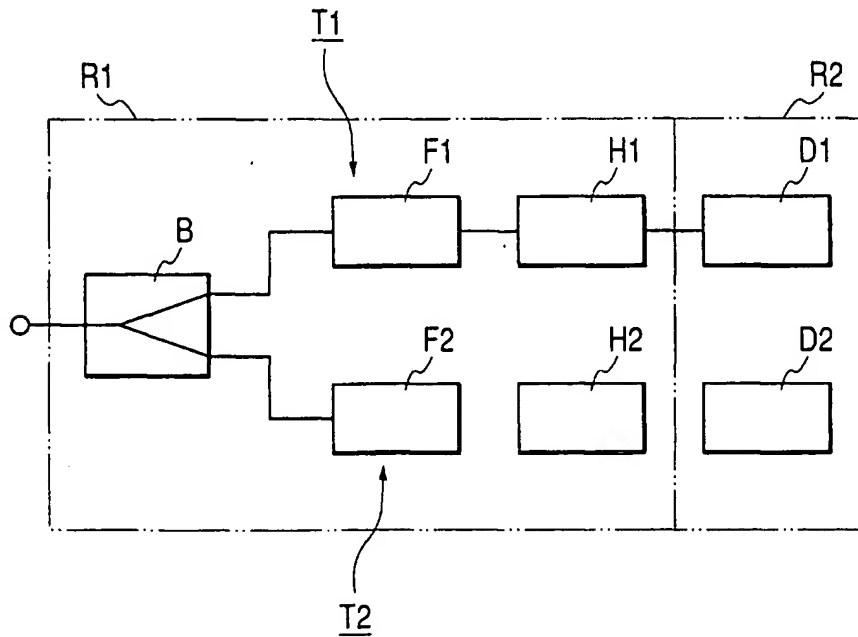
(71) Applicant: **ALPS ELECTRIC CO., LTD.**
Ota-ku Tokyo 145 (JP)

(54) **Digital broadcast receiving tuner suitable for miniaturization**

(57) A low-priced digital broadcast receiving tuner suitable for miniaturization will be provided. According to the present invention, it is possible to provide a low-priced digital broadcast receiving tuner having higher productivity as well as a less number of components

than a conventional tuner because, on one surface of an insulating board, there is formed a first tuner (T1) while, on the other surface thereof, there is formed a second tuner (T2), and therefore a single insulating board (1) can be shared.

FIG. 1



Description

[0001] The present invention relates to a preferred digital broadcast receiving tuner for use with a television set with a VCR or the like.

[0002] In a conventional digital broadcast receiving tuner, for example, a digital broadcast receiving tuner for use with a television set with a VCR, two tuners of a tuner for television and a tuner for a VCR are individually prepared respectively, and these two tuners are attached to a mother board respectively.

[0003] Thus, telecasting is adapted to be received by a tuner for a television, while the telecasting is picture-recorded by a tuner for a VCR, and the tuner for a VCR is adapted to, while receiving television by the tuner for a television, be able to picture record broadcast through the same channel or another channel.

[0004] The conventional digital broadcast receiving tuner has a problem that it cannot be miniaturized partly because two tuners are individually manufactured which causes low productivity and high cost, and partly because those two tuners are installed side by side on one surface of a mother board which causes a large occupied area for mounting.

[0005] Thus it is an object of the present invention to provide a low-priced digital broadcast receiving tuner suitable for miniaturization.

[0006] As a first solving means to solve the above-described problem, a structure is arranged such that the digital broadcast receiving tuner has an insulating board provided with a wiring pattern each on both surfaces thereof, and that on one surface of this insulating board there is formed a first tuner while on the other surface thereof there is formed a second tuner.

[0007] Also, as second solving means, the insulating board is structured of a stacked multi-layer board, and the structure is arranged such that the first and second tuners are electrically shielded from each other by a grounding conductor layer provided within the multi-layer board.

[0008] Also, as third solving means, a structure is arranged such that the first and second tuners have a high-frequency unit and a demodulation unit respectively, and that the high-frequency unit of the first tuner and the demodulation unit of the second tuner, and the demodulation unit of the first tuner and the high-frequency unit of the second tuner are arranged at a position opposite to each other with the multi-layer board interposed therebetween respectively.

[0009] Also, as fourth solving means, a structure is arranged such that the multi-layer board is formed of at least three layers; between the lamination layers, there are provided at least the two grounding conductor layers; in a region in which the high-frequency unit is provided, the grounding conductor layers arranged near the high-frequency unit are provided with a deletion unit; and in a range in which the demodulation unit is provided, the grounding conductor layers arranged near the

demodulation unit are provided with a first remainder to thereby increase a facing distance between the wiring pattern of the high-frequency unit and the first remainder.

[0010] Also, as fifth solving means, a structure is arranged such that the high-frequency unit has an IC component having a direct conversion unit including an oscillator and a mixer, and that the grounding conductor layer arranged near the high-frequency unit is provided with a second remainder to oppose a lower portion of the IC component.

[0011] An embodiment of the present invention will now be described, by way of example only, with reference to the accompanying drawings, in which:

Fig. 1 is a block diagram showing a digital broadcast receiving tuner according to the present invention;

Fig. 2 is an enlarged cross-sectional view for a principal part showing an outline of the digital broadcast receiving tuner according to the present invention;

Fig. 3 is an enlarged plan view for a principal part showing an outline of the digital broadcast receiving tuner according to the present invention; and

Fig. 4 is an enlarged bottom view for a principal part showing an outline of the digital broadcast receiving tuner according to the present invention.

[0012] A description will be made of the drawings of the digital broadcast receiving tuner according to the present invention. Fig. 1 a block diagram showing a digital broadcast receiving tuner according to the present invention, Fig. 2 is an enlarged cross-sectional view for a principal part showing an outline of the digital broadcast receiving tuner according to the present invention, Fig. 3 is an enlarged plan view for a principal part showing an outline of the digital broadcast receiving tuner according to the present invention, and Fig. 4 is an enlarged bottom view for a principal part showing an outline of the digital broadcast receiving tuner according to the present invention.

[0013] Next, a description will be made of the structure of the digital broadcast receiving tuner according to the present invention by exemplifying a tuner for use with, for example, a television set with a VCR. As shown in the block diagram of Fig. 1, the digital broadcast receiving tuner has two tuners of a first tuner T1 for a television and a second tuner T2 for a VCR, and the respective first and second tuners T1 and T2 are composed of filters F1 and F2, direct conversion units H1 and H2, each including an oscillator and a mixer, and demodulation units D1 and D2.

[0014] Thus, an inputted signal is distributed to the first and second tuners T1 and T2 by a distributor B, and in the first tuner T1 for a television, the signal is adapted to be transmitted to the demodulation unit D1 through

the filter F1 and the direct converter H1, and in the second tuner T2 for a VCR, the signal is adapted to be transmitted to the demodulation unit D2 through the filter F2 and the direct converter H2.

[0015] Also, each of these first and second tuners T1 and T2 is structured of a high-frequency region R1 consisting of the distributor B, the filters F1 and F2 and the direct converters H1 and H2, and a demodulation unit region R2 consisting of the demodulation units D1 and D2.

[0016] Figs. 2 to 4 show a digital broadcast receiving tuner according to the present invention, structured in a state in which the distributor B in the block diagram of Fig. 1 is excluded.

[0017] Next, with reference to Figs. 2 to 4, a description will be made of the structure of the digital broadcast receiving tuner according to the present invention. An insulating board 1, which is a circuit board, is a multi-layer board formed by stacking a plurality (three layers in this case) of boards 1a, 1b and 1c, and on both surfaces, namely, a surface and a back surface exposed of this insulating board 1, there are provided wiring patterns 2a and 2b.

[0018] Thus, on one surface side, which is the surface of this insulating board 1, there is formed the first tuner T1 for a television, and on the other surface side, which is the back surface, there is formed the second tuner T2 for a VCR.

[0019] This first tuner T1 for a television is, as shown in Figs. 2 and 3, structured such that in the high-frequency unit region R1 of the insulating board 1, there are arranged the filter F1 and the IC component 3a having the direct conversion unit H1 including the oscillator and the mixer, and that in the demodulation unit region R2, there is arranged the IC component 4a having the demodulation unit D1.

[0020] Also, the second tuner T2 for a VCR is, as shown in Figs. 2 and 4, structured such that in the high-frequency unit region R1 of the insulating board 1, there are arranged the filter F2 and the IC component 3b having the direct conversion unit H2 including the oscillator and the mixer, and that in the demodulation unit region R2, there is arranged the IC component 4b having the demodulation unit D2.

[0021] The first and second tuners T1 and T2 are structured such that the high-frequency unit region R1 of the first tuner T1 and the demodulation unit region R2 of the second tuner T2, and the demodulation unit region R2 of the first tuner T1 and the high-frequency unit region R1 of the second tuner T2 are arranged at a position opposite to each other with the insulating board 1, which is the multi-layer board, interposed therebetween respectively.

[0022] Also, between the lamination layers of the insulating board 1 formed into three layers by the boards 1a, 1b and 1c, there are provided two grounding conductor layers 5 and 6, and the grounding conductor layer 5 provided between the board 1a and the board 1b is

provided near the first tuner T1 side, while the grounding conductor layer 6 provided between the board 1b and the board 1c is provided near the second tuner T2 side.

[0023] These grounding conductor layers 5 and 6 electrically shield between the first tuner T1 and the second tuner T2.

[0024] Also, the grounding conductor layer 5 is, as shown in Fig. 2, structured such that there is provided a first remainder 5a on the demodulation unit region R2 side; the high-frequency unit region R1 is provided with a deletion unit 5b; and there is further provided a second remainder 5c to oppose to the lower portion of the IC component 3a having the direct conversion unit H1.

[0025] Also, the grounding conductor layer 6 is, as shown in Fig. 2, structured such that there is provided a first remainder 6a on the demodulation unit region R2 side; the high-frequency unit region R1 is provided with a deletion unit 6b; and there is further provided a second remainder 6c to oppose to the lower portion of the IC component 3b having the direct conversion unit H2.

[0026] Thus, although not shown here, the structure is arranged such that the second remainder 5c is connected to the first remainder 6a, and the second remainder 6c is connected to the first remainder 5a, through a connecting conductor, with which the through-hole is filled, or the like.

[0027] Particularly, in the high-frequency unit of the first tuner T1 for a television having such a structure, the deletion unit 5b is provided whereby capacity between the wiring pattern 2a and the grounding conductor layer 6a is reduced to improve the performance of the first tuner T1, and the first remainders 5a and 6a of the grounding conductor layers 5 and 6 electrically shield between the first and second tuners T1 and T2.

[0028] Also, the IC component 3a is structured so as to incorporate a wiring portion (not shown) by an exterior portion (not shown) made of synthetic resin or the like. When this IC component 3a is connected to the wiring pattern 2a, the wiring portion of the IC component 3a retracts from the surface of the insulating board 1 so that the second remainder 5c located at the lower portion of the IC component 3a opposes to the wiring portion of the IC component 3a located at a more distant position than the wiring pattern 2a.

[0029] In the first tuner T1 for a television, therefore, the capacity between the first tuner T1 and the remainder 5c is small; the influence on the performance can be reduced; the distance between the second remainder 5c and the wiring pattern 2a can be shortened; and a grounding course of the circuit in the high-frequency unit can be shortened, thus making it possible to ensure the grounding effect.

[0030] Also, particularly, in the high-frequency unit of the second tuner T2 for a VCR, the deletion unit 6b is provided whereby capacity between the wiring pattern 2b and the grounding conductor layer 5a is reduced to improve the performance of the second tuner T2; and the IC component 3b is, as in the case of the IC compo-

nent 3a, structured so as to incorporate a wiring portion (not shown) by an exterior portion (not shown) made of synthetic resin or the like. When this IC component 3b is connected to the wiring pattern 2b, the wiring portion of the IC component 3b retracts from the surface of the insulating board 1 so that the second remainder 6c located at the lower portion of the IC component 3b opposes to the wiring portion of the IC component 3b located at a more distant position than the wiring pattern 2b.

[0031] In the second tuner T2 for a VCR, therefore, the capacity between the second tuner T2 and the remainder 6c is small; the influence on the performance can be reduced; the distance between the second remainder 6c and the wiring pattern 2b can be shortened; and the grounding course of the circuit in the high-frequency unit can be shortened, thus making it possible to ensure the grounding effect.

[0032] In this respect, in the above-described embodiment, the description has been made of an embodiment in which no distributor B is provided, but this distributor B may be provided on the insulating board 1.

[0033] According to the present invention, it is possible to provide a lower-priced digital broadcast receiving tuner having higher productivity as well as a less number of components than a conventional tuner because, on one surface of the insulating board 1, there is formed the first tuner T1 while, on the other surface thereof, there is formed the second tuner T2, and therefore a single insulating board 1 can be shared.

[0034] Also, when mounting the digital broadcast receiving tuner according to the present invention onto a mother board, the occupied area can be made smaller and a smaller-sized tuner can be obtained than the conventional one.

[0035] Also, since the insulating board 1 is structured of a stacked multi-layer board, and the first and second tuners T1 and T2 are electrically shielded from each other by the grounding conductor layers 5 and 6 provided within this multi-layer board, it is possible to provide a digital broadcast receiving tuner having good performance.

[0036] Also, since a structure is arranged such that the first and second tuners T1 and T2 have a high-frequency unit and a demodulation unit respectively, and the high-frequency unit of the first tuner T1 and the demodulation unit of the second tuner T2, and the demodulation unit of the first tuner T1 and the high-frequency unit of the second tuner T2 are arranged at a position opposite to each other with the multi-layer board interposed therebetween respectively, it is possible to keep the high-frequency unit of the first tuner T1 from that of the second tuner T2 by a large distance, and both of them less interfere with each other, thus making it possible to provide a digital broadcast receiving tuner with good performance.

[0037] Also, the multi-layer board is formed of at least three layers; between the lamination layers, there are

provided at least two grounding conductor layers 5 and 6; in a region R1 in which the high-frequency unit is provided, the grounding conductor layers 5 and 6 arranged near the high-frequency unit are provided with deletion units 5b and 6b; and in a range R2 in which the demodulation unit is provided, the grounding conductor layers 5 and 6 arranged near the demodulation unit are provided with first remainders 5a and 6a to increase a facing distance between the wiring patterns 2a and 2b of the high-frequency unit and the first remainders 5a and 6a, and therefore, the capacity between the wiring patterns 2a and 2b and the grounding conductor layers 5 and 6 can be reduced to thereby enhance the performance of the first and second tuners T1 and T2.

[0038] In addition, the high-frequency unit has IC components 3a and 3b, each having a direct conversion unit including an oscillator and a mixer, and the grounding conductor layer 5, 6 arranged near the high-frequency unit is provided with a second remainder 5c, 6c to oppose the lower portion of the IC component 3a, 3b. Therefore, the capacity between the IC component 3a, 3b and the remainder 6c is small; the influence on the performance can be reduced; the distance between the second remainder 5c, 6c and the wiring pattern 2a, 2b can be shortened; and the grounding course of the circuit in the high-frequency unit can be shortened, thus making it possible to ensure the grounding effect.

30 Claims

1. A digital broadcast receiving tuner, where in the digital broadcast recording tuner has an insulating board provided with a wiring pattern each on both surfaces thereof, and wherein on one surface of the insulating board there is formed a first tuner while on the other surface thereof there is formed a second tuner.
2. The digital broadcast receiving tuner according to Claim 1, wherein the insulating board is structured of a stacked multi-layerboard, and the first and second tuners are electrically shielded from each other by a grounding conductor layer provided within the multi-layer board.
3. The digital broadcast receiving tuner according to Claim 2, wherein the first and second tuners have a high-frequency unit and a demodulation unit respectively, and wherein the high-frequency unit of the first tuner and the demodulation unit of the second tuner, and the demodulation unit of the first tuner and the high-frequency unit of the second tuner are arranged at a position opposite to each other with the multi-layer board interposed therebetween respectively.
4. The digital broadcast receiving tuner according to

Claim 3, wherein the multi-layer board is formed of at least three layers, wherein between lamination layers, there are provided at least the two grounding conductor layers, wherein in a region in which the high-frequency unit is provided, the grounding conductor layers arranged near the high-frequency unit are provided with deletion units; and wherein in a range in which the demodulation unit is provided, the grounding conductor layers arranged near the demodulation unit are provided with a first remainder to thereby increase a facing distance between the wiring pattern of the high-frequency unit and the first remainder.

5. The digital broadcast receiving tuner according to Claim 4, wherein the high-frequency unit has an IC component having a direct conversion unit including an oscillator and a mixer, and the grounding conductor layer arranged near the high-frequency unit is provided with a second remainder to oppose a lower portion of the IC component.

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FIG. 1

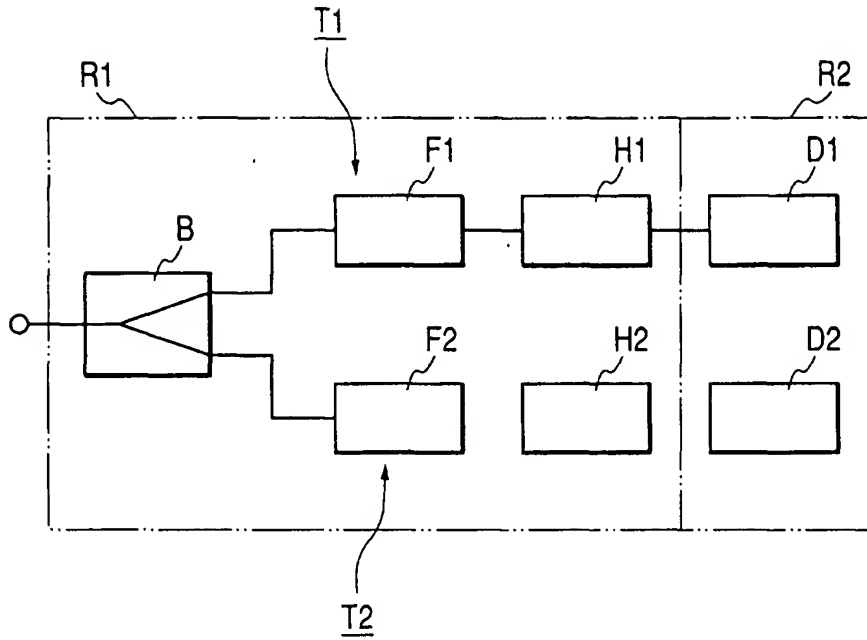


FIG. 2

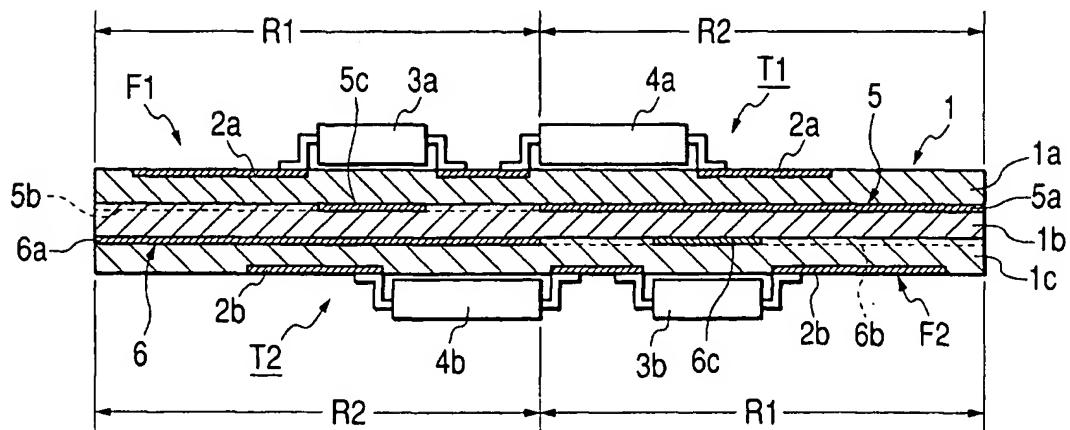


FIG. 3

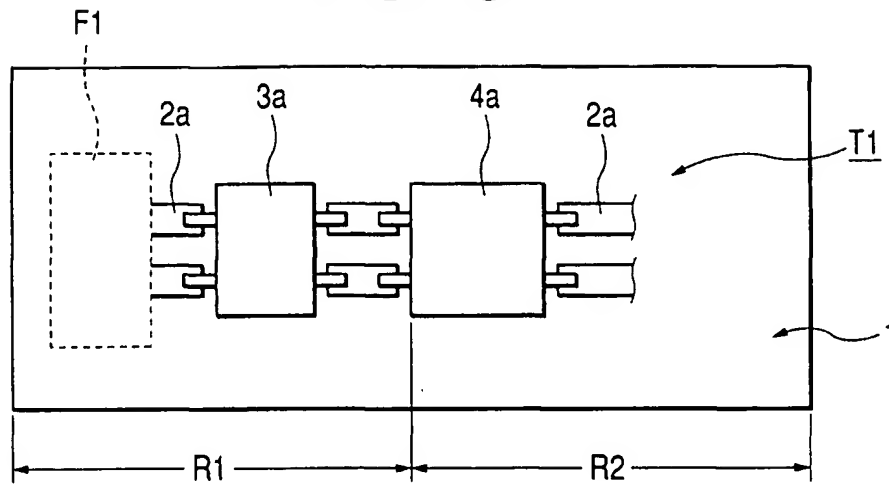
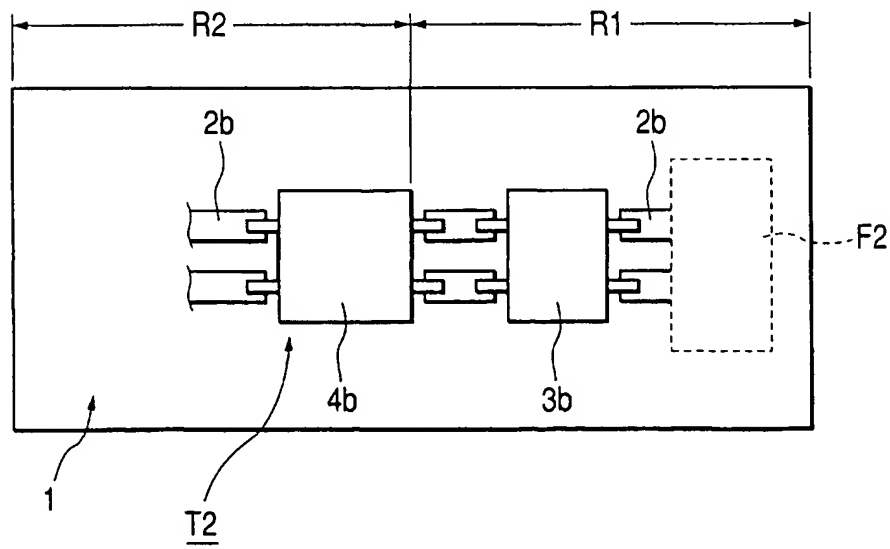


FIG. 4





(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:
28.01.2004 Bulletin 2004/05

(51) Int Cl.7: **H04N 5/44, H05K 1/02**

(43) Date of publication A2:
02.05.2002 Bulletin 2002/18

(21) Application number: **01308727.5**

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c/o Alps Electrical Co. Ltd.
Tokyo (JP)

(30) Priority: **26.10.2000 JP 2000332143**

(74) Representative: **Kensett, John Hinton**
Saunders & Dolleymore,
9 Rickmansworth Road
Watford, Hertfordshire WD18 0JU (GB)

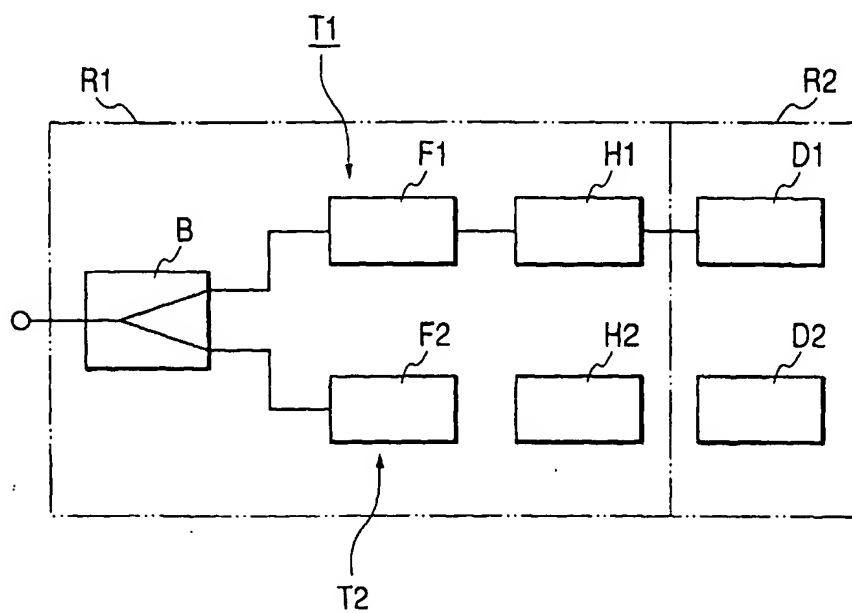
(71) Applicant: **ALPS ELECTRIC CO., LTD.**
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than a conventional tuner because, on one surface of an insulating board, there is formed a first tuner (T1) while, on the other surface thereof, there is formed a second tuner (T2), and therefore a single insulating board (1) can be shared.

FIG. 1





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EUROPEAN SEARCH REPORT

Application Number
EP 01 30 8727

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
Y	PATENT ABSTRACTS OF JAPAN vol. 1999, no. 01, 29 January 1999 (1999-01-29) & JP 10 290055 A (SONY CORP), 27 October 1998 (1998-10-27) * abstract *	1-5	H04N5/44 H05K1/02
Y	--- PATENT ABSTRACTS OF JAPAN vol. 1997, no. 01, 31 January 1997 (1997-01-31) & JP 08 228129 A (MATSUSHITA ELECTRIC IND CO LTD), 3 September 1996 (1996-09-03) * abstract *	1-5	
A	--- DE 196 49 433 A (ALPS ELECTRIC CO LTD) 5 June 1997 (1997-06-05) -----		
			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			H04N H05K
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 5 December 2003	Examiner Yvonnet, J
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EPO FORM 1503 (03.02) (P4/C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 01 30 8727

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05-12-2003

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
JP 10290055	A	27-10-1998	NONE	
JP 08228129	A	03-09-1996	JP 3099668 B2	16-10-2000
DE 19649433	A	05-06-1997	JP 3277979 B2	22-04-2002
			JP 9153697 A	10-06-1997
			DE 19649433 A1	05-06-1997
			KR 246657 B1	15-03-2000

EPO FORM P0459

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